

File 348:EUROPEAN PATENT 1978-2001/APR W05

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File 349:PCT Fulltext 1983-2001/UB=20010419, UT=20010405

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Set	Items	Description
S1	13937	VOCODER OR VODER OR (VOICE? OR SPEECH) (3N) (SYNTHE? OR RESYN- NTH? OR CODE? ? OR CODING OR DECOD??? OR ENCOD???) OR (SOUND? OR VERBAL OR VOCAL? OR SING? OR WORD? ?) (3N) (SYNTHE? OR RESYN- THE?)
S2	215667	VECTOR? OR DSP OR DSPS OR SIGNAL? ?(1N)PROCESS???? OR MATR- IX? OR MATRICES OR ARRAY?(1W)PROCESS???
S3	83548	SCALAR? OR PROTOCOL?
S4	58639	MULTIPROCESS? OR (MULTI OR MULTIPLE OR MANY OR SEVERAL OR - PLURAL? OR NUMEROUS OR MORE(1W)ONE OR THREE) (4W) (PROCESS???? - OR MICROPROCESS????)
S5	24079	(CELL OR CELLULAR OR MOBILE OR PORTABLE OR WIRELESS OR HAN- DHELD OR HAND()HELD OR CORDLESS OR RADIO OR WITHOUT(2W) (CORD? ? OR WIRE OR WIRES)) (5W) (TELEPHONE? OR PHONE OR PHONES) OR RA- DIOPHONE? OR RADIOTELEPHONE?
S6	2	S1(S)S2(S)S3(S)S4(S)S5
S7	89	S5(S)S4(S) (S1 OR S2 OR S3)
S8	82	S7 NOT AD=(19990101:20010504)/PR
S9	46	S8 NOT AD=(19970101:19981231)/PR
S10	24	S9 NOT AD=(19950101:19961231)/PR
S11	9	S10 NOT AD=(19930101:19941231)/PR
S12	4	S11 NOT AD=(19910627:19921231)/PR
S13	0	AU=(GHAUVEL GERARD? AND AUSSDAT FRANCIS? AND CALIPPE PIER- RE?)
S14	1	AU=(GHAUVEL GERARD? OR AUSSDAT FRANCIS? OR CALIPPE PIERRE- ?)
S15	2643	PA=TEXAS INSTRUMENT?
S16	2644	S15 OR S14
S17	20	S16 AND (PROTOCOL?(2N)PROCESSOR? OR S4(S)S5)
S18	20	S17 NOT AD=(19990101:20010504)/PR
S19	18	S18 NOT AD=(19970101:19981231)/PR
S20	14	S19 NOT AD=(19950101:19961231)/PR
S21	13	S20 NOT AD=(19930101:19941231)/PR
S22	10	S21 NOT AD=(19910627:19921231)/PR
?		

6/3,K/1 (Item 1 from file: 349)  
DIALOG(R) File 349:PCT Fulltext  
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00761422

**A SYSTEM, METHOD, AND ARTICLE OF MANUFACTURE FOR BUSINESS ALLIANCE  
IDENTIFICATION IN A WEB ARCHITECTURE FRAMEWORK  
SYSTEME, PROCEDE ET ARTICLE DE PRODUCTION POUR L'IDENTIFICATION D'ALLIANCES  
COMMERCIALES DANS UN CADRE D'ARCHITECTURE RESEAU**

Patent Applicant/Assignee:

ANDERSEN CONSULTING LLP, 100 South Wacker Drive, Chicago, IL 60606, US,  
US (Residence), US (Nationality)

Inventor(s):

GUHEEN Michael F, 2218 Mar East Street, Tiburon, CA 94920, US  
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Legal Representative:

BRUESS Steven C, Merchant, Gould, Smith, Edell, Welter & Schmidt, P.A.,  
P.O. Box 2903, Minneapolis, MN 55402-0903, US

Patent and Priority Information (Country, Number, Date):

Patent: WO 200073928 A2 20001207 (WO 0073928)  
Application: WO 2000US14375 20000524 (PCT/WO US0014375)  
Priority Application: US 99320816 19990527

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE  
DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC  
LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI  
SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 143341

Fulltext Availability:

Detailed Description

Detailed Description

... a label or even a short paragraph. For example, this may be the case  
if complex interest rate calculations are to be performed by the **process**  
. An elementary process description may be required for each such  
process. The process modeling component should include tools that enable  
the description to be documented...requirement.

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J) Is the extent of change to the business particularly large such that a  
detailed requirements model is needed? The requirements model (event,  
**process**, and data models) provides a clear means of depicting the  
system. The requirements model summarizes the relationship between  
events, data, and processes. It consists of...screens.

The Control-Action-Response (CAR) diagram is a commonly used technique  
for specifying the design of GUI windows. It is typically developed using  
a **matrix** or spreadsheet tool such as Microsoft Excel.

The majority of Netcentric systems use Web browsers to provide a common  
cross platform user interface. Presentation design...

6/3,K/2 (Item 2 from file: 349)  
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00394726

**MULTI-MEDIA INTERFACE  
INTERFACE MULTIMEDIA**

Patent Applicant/Assignee:

ERICSSON MESSAGING SYSTEMS INC

Inventor(s):

HULEN John S

OREN David

Patent and Priority Information (Country, Number, Date):

Patent: WO 9526088 A1 19950928

Application: WO 95US2710 19950310 (PCT/WO US9502710)

Priority Application: US 94216104 19940322

Designated States: AU CA CN JP AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT  
SE

Publication Language: English

Fulltext Word Count: 7842

Fulltext Availability:

Detailed Description

Detailed Description

... interface module and a time slot interchanger frame the received information and selectively route various time slots of information from the communications network to/from **multiple** parallel digital **signal processors (DSPs)** (each with its own dual port, high speed RAM) to perform various **protocol** conversions. A local central processing unit (CPU) controls and coordinates the line interface, time slot interchanger, and **DSPs** via a local bus in accordance with commands from the host messaging center.

The programmable line interface module links subscriber communications information received from...

12/3,K/1 (Item 1 from file: 348)  
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01275686

Intelligent power management for distributed processing systems  
Intelligente Leistungssteuerung in verteilten Verarbeitungssystemen  
Methode de gestion intelligente de puissance pour systemes de traitement  
distribues

PATENT ASSIGNEE:

Texas Instruments Incorporated, (2688370), 7839 Churchill Way, Dallas,  
Texas 75251, (US), (Applicant designated States: all)

INVENTOR:

D'Inverno, Dominique Benoit Jacques, 47, chemin des Basses Ginestieres,  
06270 Villeneuve-Loubet, (FR)  
Chauvel, Gerard, Val Bosquet, 292, Chemin du Val Bosquet, Ville Numero  
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LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 1096360 A1 010502 (Basic)

APPLICATION (CC, No, Date): EP 99402655 991025;

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-001/32

ABSTRACT WORD COUNT: 100

NOTE:

Figure number on first page: 2

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200118	666
SPEC A	(English)	200118	3514
Total word count - document A			4180
Total word count - document B			0
Total word count - documents A + B			4180

...SPECIFICATION FIELD

This invention relates in general to integrated circuits and, more particularly, to managing power in a processor.

## 2. DESCRIPTION OF THE RELATED ART

For **many** years, the focus of **processor** design, including designs for microprocessor units (MPUs), co-processors and digital **signal processors** (DSPs ), has been to increase the speed and functionality of the processor. Presently, power consumption has become a serious issue. Importantly, maintaining low power consumption, without...

...and functionality, has moved to the forefront in many designs. Power consumption has become important in many applications because many systems, such as smart phones, **cellular phones** , PDAs (personal digital assistants), and handheld computers operate from a relatively small battery. It is desirable to maximize the battery life in these systems, since...

12/3,K/2 (Item 2 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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01234606

Signal quality determination in a radio communications apparatus  
Empfangssignalqualitätsbestimmung in einem Funkkommunikationsgerat  
Determination de la qualite d'un signal dans un appareil de communication  
radio

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INC., (279076), 13500 North Central Expressway, Dallas, Texas 75243, US\ (Applicant designated states: , DE; FI; GB; IT; NL; SE)  
TEXAS INSTRUMENTS FRANCE, (460913), 821, avenue Jack Kilby, B.P. 5, 06271 Villeneuve Loubet Cedex, FR\ (Applicant designated states: , AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE)

INVENTOR:

Thomas, David R., 15 Chemin du bois d'Opio, 06650 Opio, (FR)

LEGAL REPRESENTATIVE:

Potter, Julian Mark (80064), D. Young & Co., 21 New Fetter Lane, London EC4A 1DA, (GB)

PATENT (CC, No, Kind, Date): EP 1069713 A1 010117 (Basic)

APPLICATION (CC, No, Date): EP 99401573 990623;

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: H04B-017/00; H04B-007/02; H01Q-003/26; H04Q-007/36; H04B-007/04

ABSTRACT WORD COUNT: 40

NOTE:

Figure number on first page: 5

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200103	622
SPEC A	(English)	200103	12184
Total word count - document A			12806
Total word count - document B			0
Total word count - documents A + B			12806

...SPECIFICATION an adaptive antenna array system 1000 suitable for including in a mobile station for a radio communications network. The adaptive antenna array system 1000 comprises **three** basic sections; a baseband **processing** section 1002, a signal digitisation and conditioning section 1004 and an RF front end 1006. In this embodiment, the adaptive array 602 comprises three antenna elements 6020)), 6021)) and 6022)). The adaptive antenna array system 1000 is controlled by a digital **signal processor** 1008 of the type TMS320C541 made by Texas Instruments Incorporated. Digital **signal processor** 1008 controls the baseband processing for the mobile station, including forwarding recovered audio to a speaker, and receiving audio from a microphone input. In addition to performing the baseband processing for the mobile station, digital **signal processor** 1008 also implements the constant modulus algorithm described above for obtaining a steady state of response from the antenna array 602, thereby tracking the constant level signal such as the BCCH signal of a GSM **radio telephone** network. Digital **signal processor** 1008 may also be configured to perform other processes or algorithms, such as a set-up or an initialisation process for establishing communication with a...

12/3,K/3 (Item 3 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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01234605

Radio communications apparatus with a steerable directional beam antenna  
Funkkommunikationsgerat mit einer steuerbaren Richtstrahlantenne  
Appareil de communication radio muni d'une antenne a faisceau directionnel orientable

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INC., (279076), 13500 North Central Expressway, Dallas, Texas 75243, US\ (Applicant designated states: , DE; FI; GB; IT; NL; SE)  
TEXAS INSTRUMENTS FRANCE, (460913), 821, avenue Jack Kilby, B.P. 5, 06271 Villeneuve Loubet Cedex, FR\ (Applicant designated states: , AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE)

INVENTOR:

Thomas, David R., 15 min du bois d'Opio, 06650 Opio (FR)

LEGAL REPRESENTATIVE:

Potter, Julian Mark (80064), D. Young & Co., 21 New Fetter Lane, London  
EC4A 1DA, (GB)

PATENT (CC, No, Kind, Date): EP 1069706 A1 010117 (Basic)

APPLICATION (CC, No, Date): EP 99401571 990623;

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: H04B-007/02; H04B-007/04

ABSTRACT WORD COUNT: 67

NOTE:

Figure number on first page: 05

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200103	1547
SPEC A	(English)	200103	12454
Total word count - document A			14001
Total word count - document B			0
Total word count - documents A + B			14001

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12/3,K/4 (Item 1 from file: 349)

DIALOG(R)File 349:PCT Fulltext

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00231816

**DIRECTION FINDING LOCALIZATION SYSTEM**

**SYSTEME DE LOCALISATION UTILISANT LA RADIOGONIOMETRIE**

Patent Applicant/Assignee:

VENTANA SCIENCES INC

Inventor(s):

MALONEY John E

KATZ Charles N

Patent and Priority Information (Country, Number, Date):

Patent: WO 8801061 A1 19880211

Application: WO 87US1876 19870806 (PCT/WO US8701876)

Priority Application: US 86894867 19860808

Designated States: AT BE CH DE FR GB IT JP LU NL NO SE

Publication Language: English

Fulltext Word Count: 7233

Fulltext Availability:

Detailed Description

Detailed Description

, ... as indicated at step 216 until a direction angle measurement has been returned for each on the assumption that eventually the mobile transmitter broadcasts a **protocol** messages at step 221. These **protocol** messages may be modified, as necessary at step 222 to allocate frequency space in the service area and maintain sufficient signal strength at the sub...

...process of modifying the signal from the messages for the mobile transmitter is analogous to the hand-over process for transferring between cells in a **cellular telephone** network except that the control land station may adjust the frequency and strength of the signal from the mobile transmitter station to be received at **more** than **one** station. The foregoing **process** is repeated essentially forever as indicated at step 223 as is customary for a **mobile telephone** in a **cellular telephone** network.

At step 204 the control land station receives the measurements of the direction angles from the support land stations. The control processor of the...

22/3,K/1 (Item 1 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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01275686

Intelligent power management for distributed processing systems  
Intelligente Leistungssteuerung in verteilten Verarbeitungssystemen  
Methode de gestion intelligente de puissance pour systemes de traitement  
distribues

PATENT ASSIGNEE:

Texas Instruments Incorporated , (2688370), 7839 Churchill Way, Dallas,  
Texas 75251, (US), (Applicant designated States: all

INVENTOR:

D'Inverno, Dominique Benoit Jacques, 47, chemin des Basses Ginestieres,  
06270 Villeneuve-Loubet, (FR)

Chauvel, Gerard, Val Bosquet, 292, Chemin du Val Bosquet, Ville Numero  
20, 06600 Antibes, (FR)

LEGAL REPRESENTATIVE:

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EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-001/32

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22/3,K/2 (Item 2 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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01234606

Signal quality determination in a radio communications apparatus  
Empfangssignalqualitätsbestimmung in einem Funkkommunikationsgerat



. Determination de la qualite d'un signal dans un appareil de communication radio

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INC. , (279076), 13500 North Central Expressway,  
Dallas, Texas 75243, US\ (Applicant designated states: , DE; FI; GB; IT;  
NL; SE)

TEXAS INSTRUMENTS FRANCE , (460913), 821, avenue Jack Kilby, B.P. 5,  
06271 Villeneuve Loubet Cedex, FR\ (Applicant designated states: , AT;  
BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE

INVENTOR:

Thomas, David R., 15 Chemin du bois d'Opio, 06650 Opio, (FR)

LEGAL REPRESENTATIVE:

Potter, Julian Mark (80064), D. Young & Co., 21 New Fetter Lane, London  
EC4A 1DA, (GB)

PATENT (CC, No, Kind, Date): EP 1069713 A1 010117 (Basic)

APPLICATION (CC, No, Date): EP 99401573 990623;

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: H04B-017/00; H04B-007/02; H01Q-003/26;

H04Q-007/36; H04B-007/04

ABSTRACT WORD COUNT: 40

NOTE:

Figure number on first page: 5

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SPEC A	(English)	200103	12184
Total word count - document A			12806
Total word count - document B			0
Total word count - documents A + B			12806

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INC ...

...DE; FI; GB; IT; NL; SE)

TEXAS INSTRUMENTS FRANCE ...

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22/3,K/3 (Item 3 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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01234605

Radio communications apparatus with a steerable directional beam antenna  
Funkkommunikationsgerat mit einer steuerbaren Richtstrahlantenne  
Appareil de communication radio muni d'une antenne a faisceau directionnel  
orientable

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INC. , (279076), 13500 North Central Expressway,  
Dallas, Texas 75243, US\ (Applicant designated states: , DE; FI; GB; IT;  
NL; SE)

TEXAS INSTRUMENTS FRANCE , (460913), 821, avenue Jack Kilby, B.P. 5,

06271 Villeneuve Loubet Cedex, FR\ (Applicant designated states: , AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE)  
INVENTOR:

Thomas, David R., 15 Chemin du bois d'Opio, 06650 Opio, (FR)

LEGAL REPRESENTATIVE:

Potter, Julian Mark (80064), D. Young & Co., 21 New Fetter Lane, London EC4A 1DA, (GB)

PATENT (CC, No, Kind, Date): EP 1069706 A1 010117 (Basic)

APPLICATION (CC, No, Date): EP 99401571 990623;

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: H04B-007/02; H04B-007/04

ABSTRACT WORD COUNT: 67

NOTE:

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LANGUAGE (Publication, Procedural, Application): English; English; English  
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Total word count - document A			14001
Total word count - document B			0
Total word count - documents A + B			14001

PATENT ASSIGNEE:

**TEXAS INSTRUMENTS INC ...**

...DE; FI; GB; IT; NL; SE)

**TEXAS INSTRUMENTS FRANCE ...**

...SPECIFICATION an adaptive antenna array system 1000 suitable for including in a mobile station for a radio communications network. The adaptive antenna array system 1000 comprises **three** basic sections; a baseband **processing** section 1002, a signal digitisation and conditioning section 1004 and an RF front end 1006. In this embodiment, the adaptive array 602 comprises three antenna...

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22/3,K/4 (Item 4 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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01135879

**Hardware accelerator for data processing systems**

**Hardwarebeschleuniger für Datenbearbeitungssystem**

**Accélérateur en hardware pour systèmes de traitement de données**

PATENT ASSIGNEE:

**TEXAS INSTRUMENTS INC.** , (279076), 13500 North Central Expressway, Dallas, Texas 75243, US\ (Applicant designated states: , BE; CH; DE; DK; ES; FI; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE; AT; CY)

**TEXAS INSTRUMENTS FRANCE** , (460913), 821, avenue Jack Kilby, B.P. 5, 06271 Villeneuve Loubet Cedex, FR\ (Applicant designated states: , FR)

INVENTOR:

Laurenti, Gilbert, 1490 chemin de Saint Etienne, 06570 Saint Paul de Vence, (FR)

Giacalone, Jean-Pierre, 484 chemin des Ecoliers, 06140 Vence, (FR)

Djafarian, Karim, 453 boulevard de la Reine Jeanne, Les Toscanes, Batiment B1 06140 Vence, (FR)

Laine, Armelle, 13 rue Aubernon, 06600 Antibes, (FR)

LEGAL REPRESENTATIVE:

Potter, Julian Mark e 1 (80064), D. Young & Co., 21 Fetter Lane,  
London EC4A 1DA, (GB)  
PATENT (CC, No, Kind, Date): EP 992895 A1 000412 (Basic)  
APPLICATION (CC, No, Date): EP 98402463 981006;  
DESIGNATED STATES: DE; FI; FR; GB; SE  
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI  
INTERNATIONAL PATENT CLASS: G06F-009/38  
ABSTRACT WORD COUNT: 142  
NOTE:

Figure number on first page: 6

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200015	820
SPEC A	(English)	200015	7355
Total word count - document A			8175
Total word count - document B			0
Total word count - documents A + B			8175

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INC ...

...BE; CH; DE; DK; ES; FI; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE; AT; CY)  
TEXAS INSTRUMENTS FRANCE ...

...SPECIFICATION signal processor "DSP" (such as a TI-DSP #C54x) - coupled to a second processor 14 (such as an ARM TMS470 processor) - in this case a **protocol processor** . The core 18 of the DSP 12 is coupled to the core 20 of **protocol processor** 14 by a synchronizing circuit 16. The DSP 12 further includes a program ROM memory 22 and a local RAM memory 24. Processor 14 includes...

...hardwired logic block 52 permitting the shaping of signals for a particular processing which would be too costly to carry out by means of the **protocol processor** 32. The logic block 52 is moreover connected to the processor 32 by an interrupt line 58.

FIG. 4 shows in more detail the **protocol processor** , which in fact comprises three pans. A program part denoted with the general reference numeral 60 contains an incrementation register 62 which is incremented with...the destination address. This destination address is embodied in the diagram of FIG. 4 by a dual-port memory 88 which is common to the **protocol processor** and to the main processing unit CPU 90 with which it is associated. The memory 88 is connected to the CPU 90 by means of a data and address bus 92, 94.

One example of a **protocol processor** 14 is the ARM 7X processor. The ARM processor performs co-processor "data operations", "data transfers", and "register transfers". The processor utilizes a condition field...

...from the busy wait. As a result, there is a penalty to use it.

In the prior art (see Figs. 1-5), there is a **protocol processor** (ARM (7x) in this case) in which any change within the co-processor (DSP 12 in this case), in terms of data (via a bus, an instruction, and so forth), requires that the **protocol processor** use a memory bus and a RAM. First, processing within the co-processor (CDP) is performed. Next, a register to register transfer (MCR) is performed...

...a transfer of the (LDC/STC) - three cycles in all. Throughout all of this, the co-processor (DSP) does not see the resources inside the **protocol processor** (ARM 7X). While the co-processor (DSP 12) has its own set of resources that allows it to perform some processing on its own, everything that relates to talking to the environment of the **protocol processor** (ARM 7X) - i.e., software, the bus, the RAM, etc., is under control of the **protocol processor** (ARM 7X) with specific instructions and adds additional cycles to the time needed to perform multiple operations. What is needed is an improved technique for...

...cycle operation. Such improved technique should be useable in

conjunction with the processor (DSP) to perform some of the functionality currently performed by the **protocol processor** and in fewer instructions than is presently required, or it could be used in conjunction with a single processor system.

#### SUMMARY OF THE INVENTION

The...

22/3,K/5 (Item 5 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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00469456

Voice recognition telephone dialing  
Rufnummerwahl mit Spracherkennung  
Numerotation telefonique par reconnaissance de la parole  
PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED , (279070), 13500 North Central  
Expressway, Dallas Texas 75265, (US), (applicant designated states:  
DE;FR;GB;IT;NL;SE

INVENTOR:

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PATENT (CC, No, Kind, Date): EP 477688 A2 920401 (Basic)  
EP 477688 A3 920715  
EP 477688 B1 980729

APPLICATION (CC, No, Date): EP 91115471 910912;  
PRIORITY (CC, No, Date): US 590249 900928  
DESIGNATED STATES: DE; FR; GB; IT; NL; SE  
INTERNATIONAL PATENT CLASS: G10L-003/00; H04M-001/27;  
ABSTRACT WORD COUNT: 84

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9831	792
CLAIMS B	(German)	9831	775
CLAIMS B	(French)	9831	1026
SPEC B	(English)	9831	3296
Total word count - document A			0
Total word count - document B			5889
Total word count - documents A + B			5889

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED ...

...SPECIFICATION for communicating with database system 13.

Signal processing system 15 receives voice signal data via T1 line 12 and T1 buffer 18, according to telecommunications **protocols** . Signal **processor** system 15 executes program routines downloaded to it from host processor 14a. When execution of one program routine is complete, signal processor system 15 notifies...

22/3,K/6 (Item 6 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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00434112

Portable and dynamic distributed applications architecture.  
Übertragbare und dynamische Architektur für Verteilerverwendungen.  
Architecture portable et dynamique pour des application distribuées.  
PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED , (279070), 13500 North Central  
Expressway, Dallas Texas 75265, (US), (applicant designated states:

BE;DE;ES;FR;GB;IT;NL;SE

INVENTOR:

White, John W., 12307 Brittany Cr., Dallas, Texas 75230, (US)

LEGAL REPRESENTATIVE:

Abbott, David John et al (27491), Abel & Imray Northumberland House  
303-306 High Holborn, London, WC1V 7LH, (GB)

PATENT (CC, No, Kind, Date): EP 421624 A2 910410 (Basic)

EP 421624 A3 930310

APPLICATION (CC, No, Date): EP 90310099 900914;

PRIORITY (CC, No, Date): US 414221 890928

DESIGNATED STATES: BE; DE; ES; FR; GB; IT; NL; SE

INTERNATIONAL PATENT CLASS: G06F-009/44; G06F-009/46;

ABSTRACT WORD COUNT: 280

LANGUAGE (Publication,Procedural,Application): English; English; English

PATENT ASSIGNEE:

**TEXAS INSTRUMENTS INCORPORATED ...**

...ABSTRACT module is then compiled using the target system's compiler,  
link editor, and bind process. Thus, all environment-dependent variations  
of import/export, including network **protocol** , operating systems,  
**processor** types, etc., are automatically integrated with the application  
at load module bind time. Therefore, no source code changes are  
necessary. (see image in original document)

**22/3,K/7 (Item 7 from file: 348)**

DIALOG(R)File 348:EUROPEAN PATENTS

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00434111

**Portable and dynamic distributed applications architecture.**

**Übertragbare und dynamische Architektur für verteilte Anwendungen.**

**Architecture portable et dynamique pour des applications distribuées.**

PATENT ASSIGNEE:

**TEXAS INSTRUMENTS INCORPORATED** , (279070), 13500 North Central  
Expressway, Dallas Texas 75265, (US), (applicant designated states:  
BE;DE;ES;FR;GB;IT;NL;SE

INVENTOR:

White, John W., 12307 Brittany Cr., Dallas, Texas 75230, (US)

LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 421623 A2 910410 (Basic)

EP 421623 A3 930310

APPLICATION (CC, No, Date): EP 90310098 900914;

PRIORITY (CC, No, Date): US 414221 890928

DESIGNATED STATES: BE; DE; ES; FR; GB; IT; NL; SE

INTERNATIONAL PATENT CLASS: G06F-009/44; G06F-009/46;

ABSTRACT WORD COUNT: 279

LANGUAGE (Publication,Procedural,Application): English; English; English

PATENT ASSIGNEE:

**TEXAS INSTRUMENTS INCORPORATED ...**

...ABSTRACT module is then compiled using the target system's compiler,  
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of import/export, including network **protocol** , operating systems,  
**processor** types, etc., are automatically integrated with the application  
at load module bind time. Therefore, no source code changes are  
necessary. (see image in original document)

**22/3,K/8 (Item 8 from file: 348)**

DIALOG(R)File 348:EUROPEAN PATENTS

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00434110

, Portable and dynamic distributed applications architecture.  
Übertragbare und dynamische Architektur für verteilte Anwendungen.  
Architecture portable et dynamique pour des applications distribuées.

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED , (279070), 13500 North Central  
Expressway, Dallas Texas 75265, (US), (applicant designated states:  
BE;DE;ES;FR;GB;IT;NL;SE

INVENTOR:

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PATENT (CC, No, Kind, Date): EP 420461 A2 910403 (Basic)  
EP 420461 A3 930310

APPLICATION (CC, No, Date): EP 90310097 900914;

PRIORITY (CC, No, Date): US 414221 890928

DESIGNATED STATES: BE; DE; ES; FR; GB; IT; NL; SE

INTERNATIONAL PATENT CLASS: G06F-009/44; G06F-009/46;

ABSTRACT WORD COUNT: 279

LANGUAGE (Publication,Procedural,Application): English; English; English

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED ...

...ABSTRACT module is then compiled using the target system's compiler,  
link editor, and bind process. Thus, all environment-dependent variations  
of import/export, including network **protocol** , operating systems,  
**processor** types, etc., are automatically integrated with the application  
at load module bind time. Therefore, no source code changes are  
necessary. (see image in original document)

22/3,K/9 (Item 9 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00434109

Portable and dynamic distributed applications architecture.  
Übertragbare und dynamische Architektur für verteilte Anwendungen.  
Architecture portable et dynamique pour des applications distribuées.

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED , (279070), 13500 North Central  
Expressway, Dallas Texas 75265, (US), (applicant designated states:  
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INVENTOR:

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PATENT (CC, No, Kind, Date): EP 421622 A2 910410 (Basic)  
EP 421622 A3 930224

APPLICATION (CC, No, Date): EP 90310096 900914;

PRIORITY (CC, No, Date): US 414221 890928

DESIGNATED STATES: BE; DE; ES; FR; GB; IT; NL; SE

INTERNATIONAL PATENT CLASS: G06F-009/44; G06F-009/46;

ABSTRACT WORD COUNT: 279

LANGUAGE (Publication,Procedural,Application): English; English; English

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED ...

...ABSTRACT module is then compiled using the target system's compiler,  
link editor, and bind process. Thus, all environment-dependent variations  
of import/export, including network **protocol** , operating systems,  
**processor** types, etc., are automatically integrated with the application  
at load module bind time. Therefore, no source code changes are  
necessary. (see image in original document)

22/3,K/10 (Item 10 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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00434108

Portable and dynamic distributed applications architecture.  
Übertragbare und dynamische Architektur für verteilte Anwendungen.  
Architecture portable et dynamique pour des applications distribuées.

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED , (279070), 13500 North Central  
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PATENT (CC, No, Kind, Date): EP 423937 A2 910424 (Basic)  
EP 423937 A3 930526

APPLICATION (CC, No, Date): EP 90310095 900914;

PRIORITY (CC, No, Date): US 414221 890928

DESIGNATED STATES: BE; DE; ES; FR; GB; IT; NL; SE

INTERNATIONAL PATENT CLASS: G06F-009/44; G06F-009/46;

ABSTRACT WORD COUNT: 279

LANGUAGE (Publication,Procedural,Application): English; English; English

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED ...

...ABSTRACT module is then compiled using the target system's compiler,  
link editor, and bind process. Thus, all environment-dependent variations  
of import/export, including network **protocol** , operating systems,  
**processor** types, etc., are automatically integrated with the application  
at load module bind time. Therefore, no source code changes are  
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